ceipt dat	e: 06/30/2009	10526523 - GAU: 2							
Form PTO-1449 U.S. Department of Commerce (Rev. 8-88) Patent and Trademark Office INFORMATION DISCLOSURE CITATION (Use several sheets if necessary) Sheet 1 of 1				Attorney Docket No.: 0321.67590			Serial No.: 10/526,523		
				Applicant: Cheng					
				Filing Date: March 1, 2005			Group: 2818		
U.S. PATEN	T DOCUMENTS			•					
Examiner Initial*	Document Number	Date	Name		Class	Subclass	Filing Date If Appropriate		
FOREIGN PA	ATENT DOCUMENTS								
								Translation	
	Document Number	Date	Country			Class	Subclass	Yes	No
	1								
							1		
OTHER DOC	CUMENTS (Including Auti	or Title Date Pe	rtinant Pages	Etc.)					
OTHER DOC					terconnect (22)	nn).			
2									
3									
4	Cheng, E., Zhou, F., Yao, B., Cheng, C., and Graham, R., "Balancing the Interconnect Topology for Arrays of Processors between Cost and Power," International Conference in Computer Design, Freiburg, Germany (September 2002).								
5	Dally, W. and Towles, B., "Route Packets, Not Wires: On-Chip Interconnection Networks," IEEE Proc. of the 38th Design Automation Conf. pp. 684-689, 2001.								
6	Garg, N. and Konemann, J., "Faster and Simpler Algorithms for Multicommodity Flow and other Fractional Packing Problems," In Proc. of the 39th Annual Symposium on Foundations of Computer Science, pp. 300-309, 1998.								
7	Igarashi, M., Mitsuhashi, T., Le, A., Kazi, S. Lin, Y., Fujimura, A. and Teig, S., "A Diagonal-Interconnect Architecture and Its Application in RISC Core Design," 2002 IEE International Solid-State Circuits Conference, Session 12/TD: Digital Directions/12.								
8	Karmarkar, N., "A New Polynomial-time Algorithm for Linear Programming." Combinatorica, 4(4), pp. 373-395, 1984.								
9	Khalid, M. and Rose, J., "Experimental Evaluation of Mesh and Partial Crossbar Routing Architectures for Multi-FPGA Systems," in IFIP IWLAS '97, Grenoble, France, pp. 119-127 (December 1997).								
10	Leiserson, C., "Fat Trees 34, No. 10, pp. 892-901		orks for Hardy	vare – Efficient Su	percomputing,	' IEEE Ti	ans. On Com	puters, V	ol. C-
Examiner	/Thinh Nguyen/		Date	Considered	07/03/200	9			
*Examiner:				ot citation is in conf ed. Include copy of					